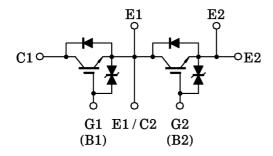
TOSHIBA GTR Module Silicon N Channel IGBT

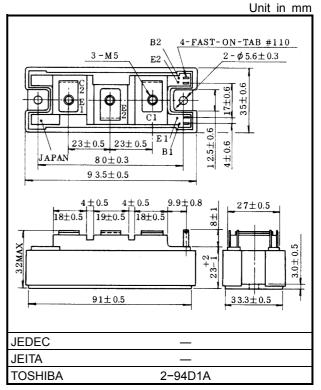
# MG50Q2YS40

High Power Switching Applications. Motor Control Applications.

- High input impedance
- High speed:  $tf = 0.5\mu s$  (max.)  $trr = 0.5\mu s$  (max.)
- Low saturation voltage
  - $: V_{\text{CE(sat)}} = 4.0V \text{ (max.)}$
- Enhancement-mode
- Includes a complete half bridge in one package.
- The electrodes are isolated from case.

### **Equivalent Circuit**





Weight: 202g

#### **Maximum Ratings (Ta = 25°C)**

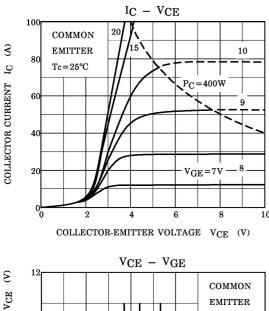
Characteristic		Symbol	Rating	Unit	
Collector–emitter voltage		V <sub>CES</sub>	1200	V	
Gate-emitter voltage		V <sub>GES</sub>	±20	٧	
Collector current	DC	IC	50	A	
	1ms	I <sub>CP</sub>	100		
Forward current	DC	lF	50	А	
	1ms	I <sub>FM</sub>	100		
Collector power dissipation (Tc = 25	PC	400	W		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-40 <b>~</b> 125	°C	
Isolation voltage	V <sub>Isol</sub>	2500 (AC 1 minute)	V		
Screw torque (terminal / mounting)		_	3/3	N·m	

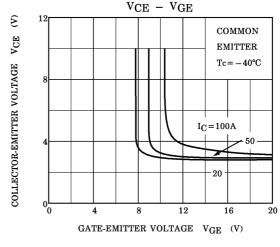
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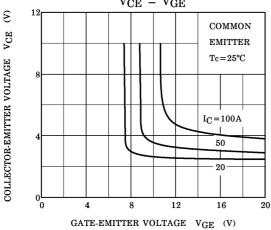
## Electrical Characteristics (Ta = 25°C)

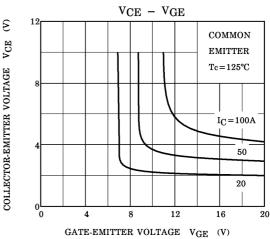
Characteristic Symb		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Gate leakage current		I <sub>GES</sub>	V <sub>GE</sub> = ±20V, V <sub>CE</sub> = 0	_	_	±10	μΑ
Collector cut-off current		I <sub>CES</sub>	V <sub>CE</sub> = 1200V, V <sub>GE</sub> = 0	_	_	1.0	mA
Gate-emitter cut-off voltage		V <sub>GE(off)</sub>	I <sub>C</sub> = 50mA, V <sub>CE</sub> = 5V	3.0	_	6.0	V
Collector–emitter saturation voltage		V <sub>CE(sat)</sub>	I <sub>C</sub> = 50A, V <sub>GE</sub> = 15V	_	3.0	4.0	V
Input capacitance		C <sub>ies</sub>	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0, f = 1MHz	_	6000	_	pF
Switching time	Rise time	t <sub>r</sub>	o	_	0.3	0.6	- µs
	Turn-on time	t <sub>on</sub>	$15V_{\square}$ $24\Omega$	_	0.4	0.8	
	Fall time	t <sub>f</sub>	0 J L_15V 7600V	_	0.2	0.5	
	Turn-off time	t <sub>off</sub>		_	0.8	1.5	
Forward voltage V <sub>F</sub>		V <sub>F</sub>	I <sub>F</sub> = 50A, V <sub>GE</sub> = 0	-	2.0	2.5	V
Reverse recovery time		t <sub>rr</sub>	I <sub>F</sub> = 50A, V <sub>GE</sub> = -10V di / dt = 100A / µs	_	0.25	0.5	μs
Thermal resistance		D	Transistor	_	_	0.31	°C/W
		R <sub>th(j-c)</sub>	Diode	_	_	1.0	_ C/W

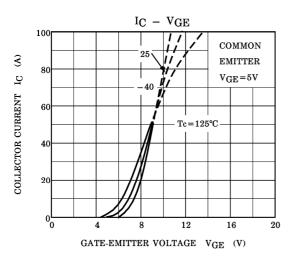
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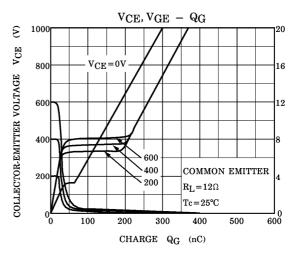




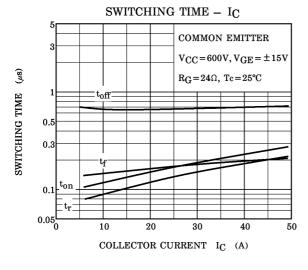


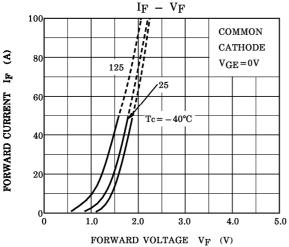


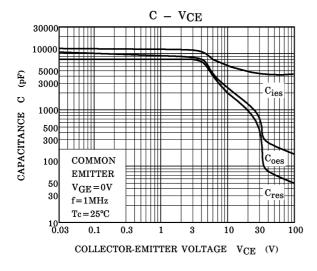


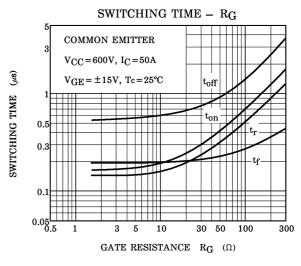


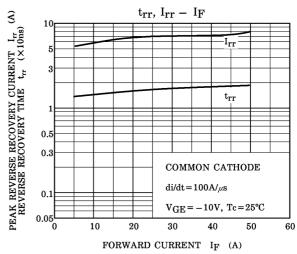
GATE-EMITTER VOLTAGE VGE (V)

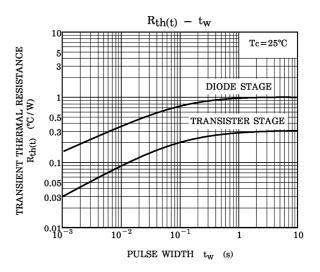


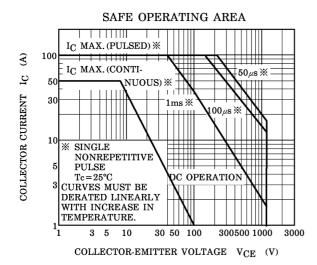


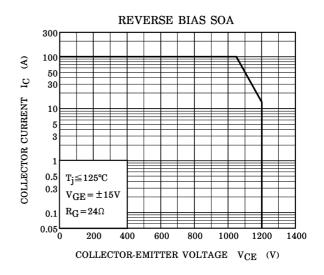












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