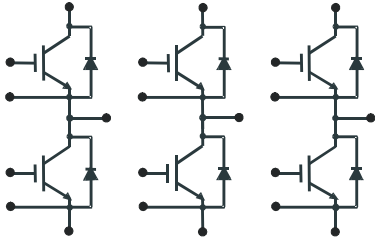


$V_{CE} = 1200\text{ V}$

$I_C = 300\text{ A}$



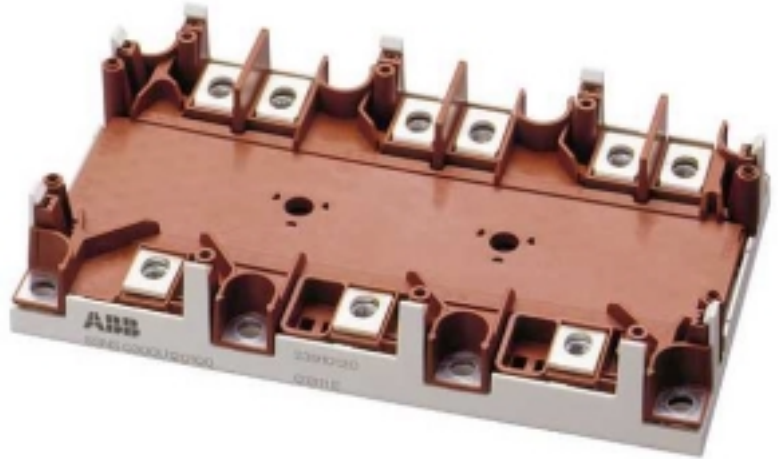
# IGBT Module LoPak5 SPT

## 5SNS 0300U120100

### PRELIMINARY

Doc. No. 5SYA1528-01 Sep. 01

- Low-loss, rugged IGBT SPT chip-set
- EMC friendly diode with positive temp. coefficient of on-state
- Low profile compact baseless package
- Snap-on PCB assembly
- Integrated PTC substrate temperature sensor



### Maximum Rated Values

( $T_{vj} = 25^\circ\text{C}$ , unless specified otherwise)

Parameter	Symbol	Conditions	Values	Unit
Collector-Emitter Voltage	$V_{CES}$	$V_{GE}$ shorted	1200	V
DC Collector Current	$I_C$	$T_{hs} = 60^\circ\text{C}$	300	A
Peak Collector Current	$I_{CM}$	Pulse: $t_p = 1\text{ms}$ , $T_{hs} = 60^\circ\text{C}$	600	A
Gate Emitter Voltage	$V_{GES}$		$\pm 20$	V
Total Power Dissipation	$P_{tot}$	$T_{hs} = 25^\circ\text{C}$ per switch	960	W
IGBT Switching SOA	SwSOA	$I_C = 600\text{ A}$ , $V_{CEM} = 1200\text{ V}$ , $V_{CC} \leq 1000\text{ V}$ , $V_{GE} = \pm 15\text{ V}$ , $T_{vj} = 125^\circ\text{C}$ voltages measured on auxiliary terminals		
IGBT Short Circuit SOA	SCSOA	$V_{CC} = 900\text{ V}$ , $V_{CEM} = 1200\text{ V}$ , $t_p = 10\ \mu\text{s}$ , $V_{GE} = \pm 15\text{ V}$ , $T_{vj} = 125^\circ\text{C}$		
DC Forward Current	$I_F$		300	A
Peak Forward Current	$I_{FM}$	Pulse: $t_p = 1\text{ms}$ , $T_{hs} = 60^\circ\text{C}$	600	A

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**Maximum Rated Values (cont.)** ( $T_{vj} = 25^{\circ}\text{C}$ , unless specified otherwise)

Parameter	Symbol	Conditions	Values	Unit
Junction Temperature	$T_{vj}$		- 40 ~ 150	$^{\circ}\text{C}$
Storage Temperature	$T_{\text{stg}}/T_{\text{cop}}$		- 40 ~ 125	$^{\circ}\text{C}$
Isolation Voltage	$V_{\text{iso}}$	1 min, f = 50Hz	2500	V
Mounting	Base to Heatsink	(M6) Hole 6.5mm diameter	2 ~ 3	Nm
	Main Terminals	M6 screws, max. insertion depth :10mm	3 ~ 5	Nm
	PCB mounting	Self tapping screw, Hole 2.5mm diameter, 6.0mm deep		
	Gate, Emitter Aux.	Spring pins, pitch of pins = 4mm, pcb thickness = 1.6mm		

**IGBT Characteristic Values** ( $T_{vj} = 25^{\circ}\text{C}$ , unless specified otherwise)

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Collector-Emitter Saturation Voltage	$V_{\text{CE(sat)}}^*$	$I_{\text{C}} = 300 \text{ A}, V_{\text{GE}} = 15 \text{ V}$	$T_{vj} = 25^{\circ}\text{C}$	1.95	2.30	V
			$T_{vj} = 125^{\circ}\text{C}$	2.10		V
Collector Cut-off Current	$I_{\text{CES}}$	$V_{\text{CE}} = 1200 \text{ V}, V_{\text{GE}} = 0 \text{ V}, T_{vj} = 125^{\circ}\text{C}$			20	mA
Gate-Emitter leakage Current	$I_{\text{GES}}$	$V_{\text{CE}} = 0 \text{ V}, V_{\text{GE}} = \pm 20 \text{ V}, T_{vj} = 125^{\circ}\text{C}$			$\pm 500$	nA
Gate-Emitter Threshold Voltage	$V_{\text{GE(To)}}$	$I_{\text{C}} = 12 \text{ mA}, V_{\text{CE}} = V_{\text{GE}}$	4.5		6.5	V
Total Gate Charge	$Q_{\text{ge}}$	$I_{\text{C}} = 300 \text{ A}, V_{\text{CE}} = 600 \text{ V}, V_{\text{GE}} = -15 \text{ to } 15 \text{ V}$		4000		nC
Input Capacitance	$C_{\text{ies}}$	$V_{\text{CE}} = 25 \text{ V}, V_{\text{GE}} = 0 \text{ V}, f = 1\text{MHz}$		27		nF
Output Capacitance	$C_{\text{oes}}$				6.5	nF
Reverse Transfer Capacitance	$C_{\text{res}}$				5.4	nF
Turn-On Delay Time	$t_{\text{d(on)}}$	$I_{\text{C}} = 300 \text{ A}, V_{\text{CC}} = 600 \text{ V}, R_{\text{gon}} = 3.3 \Omega, T_{vj} = 125^{\circ}\text{C}, V_{\text{GE}} = \pm 15 \text{ V}$		0.17		$\mu\text{s}$
Rise Time	$t_{\text{r}}$				0.08	$\mu\text{s}$
Turn-Off Delay Time	$t_{\text{d(off)}}$	$I_{\text{C}} = 300 \text{ A}, V_{\text{CC}} = 600 \text{ V}, R_{\text{goff}} = 3.3 \Omega, T_{vj} = 125^{\circ}\text{C}, V_{\text{GE}} = \pm 15 \text{ V}$		0.75		$\mu\text{s}$
Fall Time	$t_{\text{f}}$				0.07	$\mu\text{s}$
Turn-on Switching Energy	$E_{\text{on}}$	$R_{\text{gon}} = 3.3 \Omega, I_{\text{C}} = 300 \text{ A}, T_{vj} = 125^{\circ}\text{C}, V_{\text{CC}} = 600 \text{ V}, V_{\text{GE}} = \pm 15 \text{ V},$ inductive load, integrated up to: 3% $V_{\text{CE}} (E_{\text{on}}), 1\% I_{\text{C}} (E_{\text{off}})$		26.0		mJ
Turn-off Switching Energy	$E_{\text{off}}$				34.0	mJ
Module stray Inductance Plus to Minus	$L_{\text{s DC}}$				20	nH
Resistance terminal-chip	$R_{\text{CC'+EE'}}$		$T_{\text{hs}} = 25^{\circ}\text{C}$	0.85		m $\Omega$
			$T_{\text{hs}} = 125^{\circ}\text{C}$	1.0		

\* Note 1: Collector emitter saturation voltage is given at die level.

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**Diode Characteristic Values**(T<sub>vj</sub> = 25°C, unless specified otherwise)

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Voltage	V <sub>F</sub> *	I <sub>F</sub> = 300 A	T <sub>vj</sub> = 25 °C	2.00	2.40	V
			T <sub>vj</sub> = 125 °C	2.00		
Reverse Recovery Current	I <sub>rrm</sub>	I <sub>F</sub> = 300 A, R <sub>gon</sub> = 3.3 Ω, V <sub>CC</sub> = 600 V, V <sub>GE</sub> = ±15 V, T <sub>vj</sub> = 125 °C		345		A
Reverse Recovery Charge	Q <sub>rr</sub>			57		μC
Reverse Recovery Time	t <sub>rr</sub>			0.18		μs
Reverse Recovery Energy	E <sub>rec</sub>	I <sub>F</sub> = 300 A, T <sub>vj</sub> = 125 °C, V <sub>CC</sub> = 600 V, R <sub>gon</sub> = 3.3 Ω, V <sub>GE</sub> = ±15 V, inductive load, fully integrated		27		mJ
Resistance terminal-chip	R <sub>CC'+EE'</sub>		T <sub>hs</sub> = 25 °C	0.85		mΩ
			T <sub>hs</sub> = 125 °C	1.0		

\* Note 2: Forward voltage is given at die level

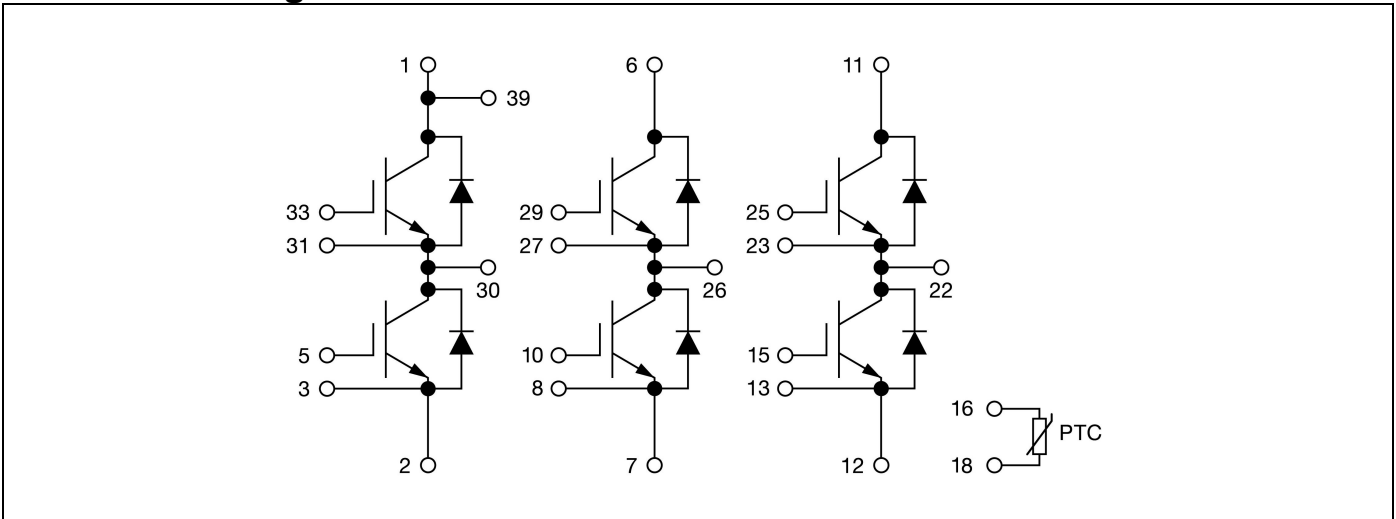
**Thermal Characteristics**(T<sub>j</sub> = 25°C, unless specified otherwise)

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
IGBT Thermal Resistance Junction to Heatsink	R <sub>th j-h</sub> Igbt	Heatsink: flatness < +/- 50 μm, roughness < 6 μm without ridge Thermal grease: thickness: 30 μm < t < 50 μm			0.130	°C/W
Diode Thermal Resistance Junction to Heatsink	R <sub>th j-h</sub> Diode				0.190	°C/W
Equivalent IGBT Thermal Resistance Junct. to Case	R <sub>th j-c</sub> Igbt				0.060	°C/W
Equivalent Diode Thermal Resistance Junct. to Case	R <sub>th j-c</sub> Diode				0.120	°C/W
Temperature sensor	PTC		Thermistor : R=1kΩ ±3%@25°C,B-value (25°C/100°C): -760K ±2%			

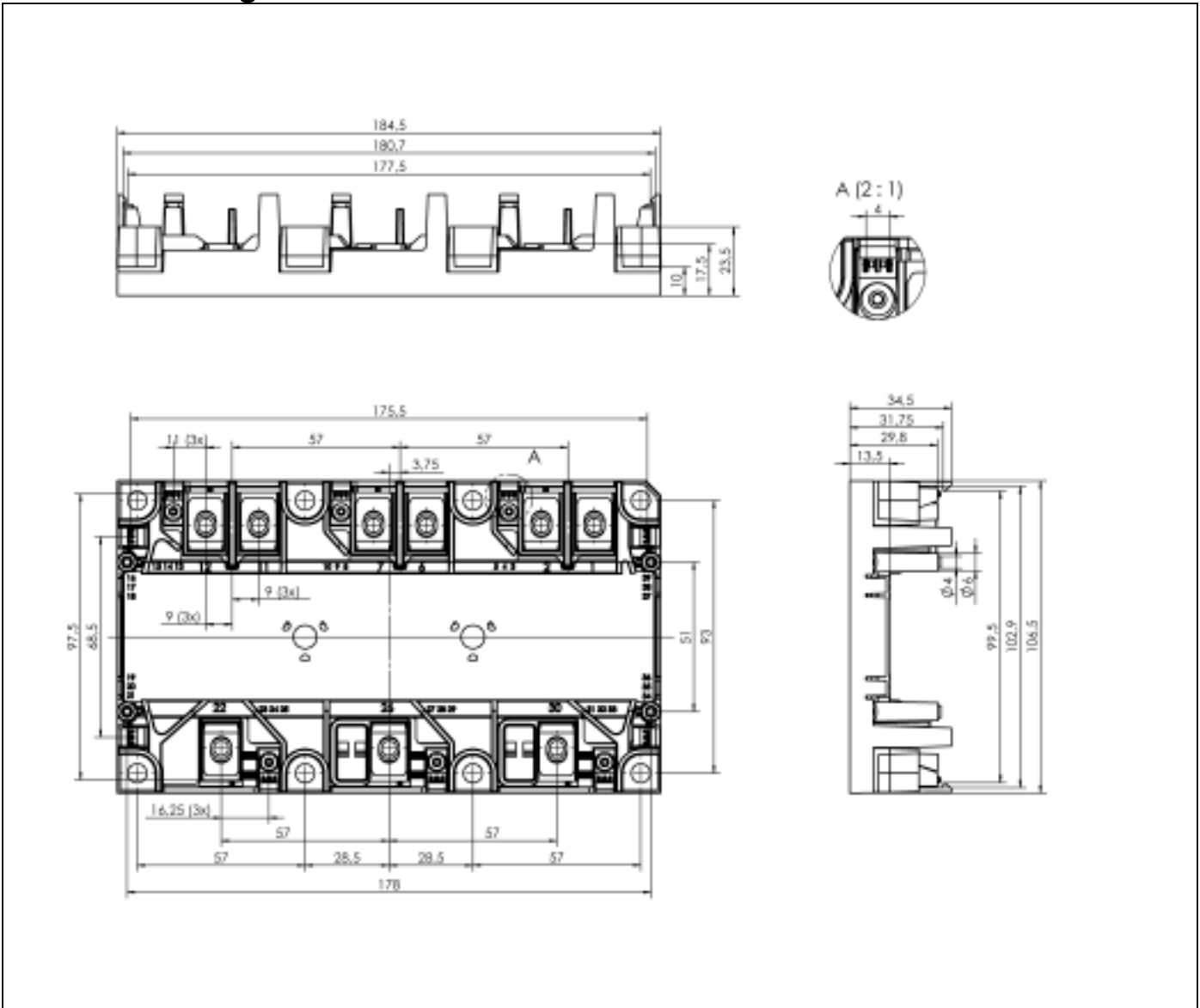
**Mechanical Properties**

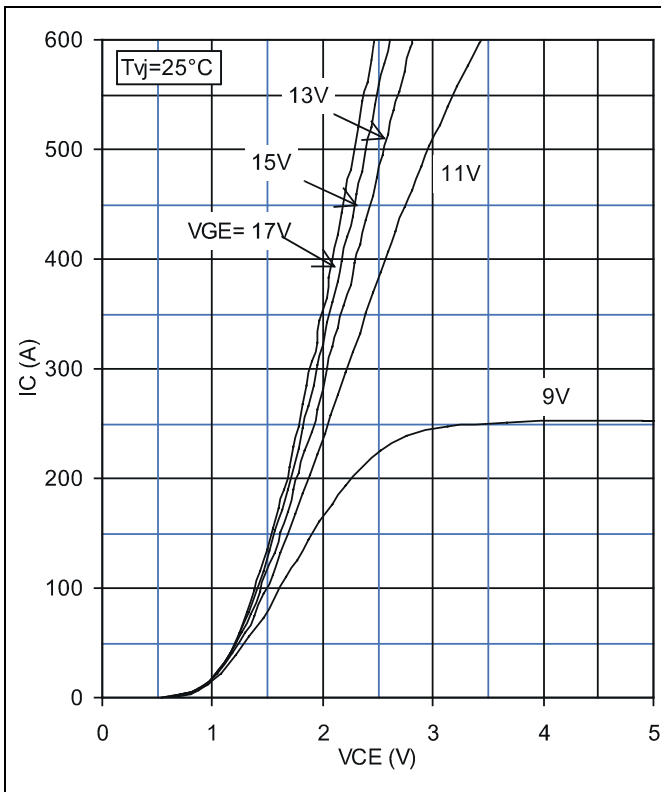
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Dimensions	L*W*H	Typical , see outline drawing	184.5 * 106.5 * 34.5			mm
Clearance Distance	D <sub>C</sub>	acc. IEC 664-1 and prEN50124-1:1995	Term. to base:	9.5		mm
			Term. to term:	11		mm
Surface Creepage Distance	D <sub>sc</sub>	acc. IEC 664-1 and prEN50124-1:1995	Term. to base:	9.5		mm
			Term. to term:	12.5		mm
Weight				485		gr

### Electrical configuration

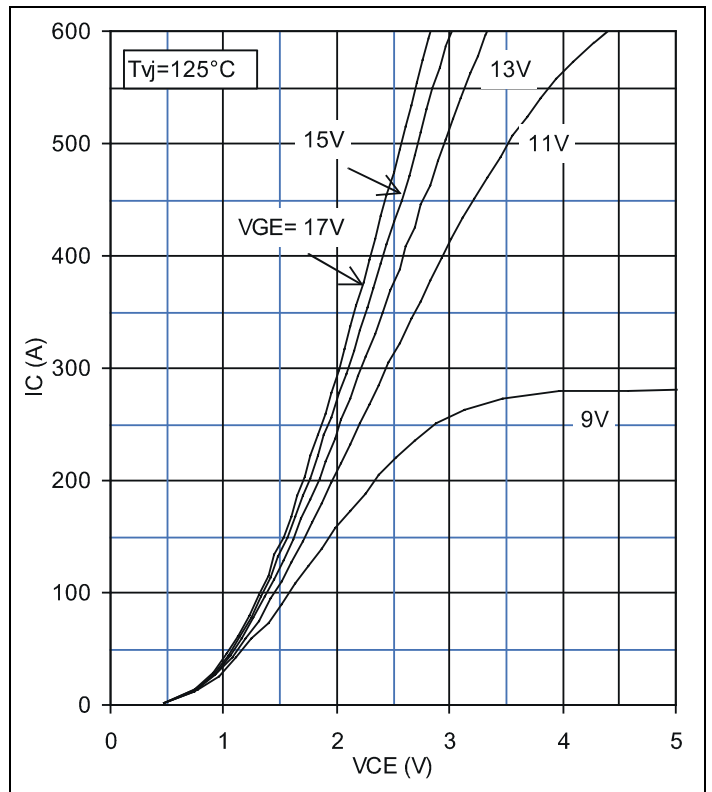


### Outline drawing

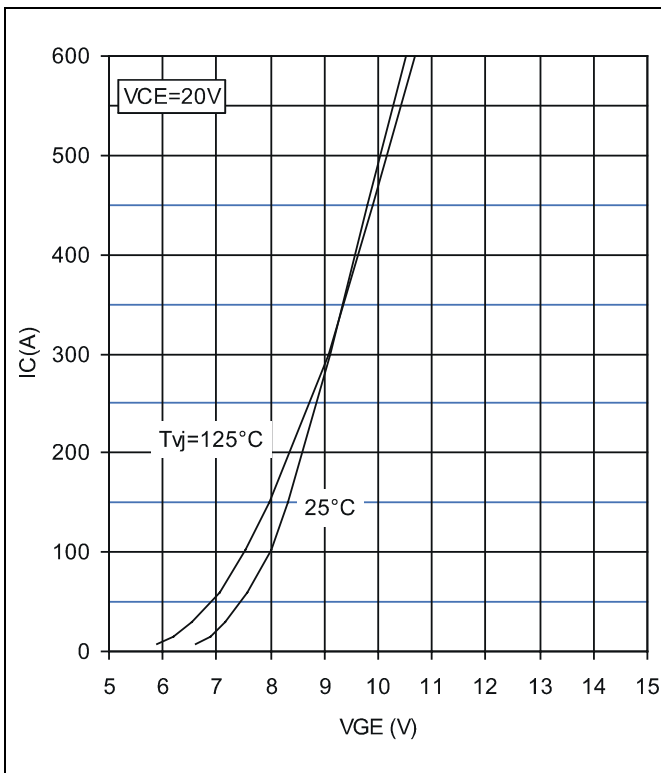




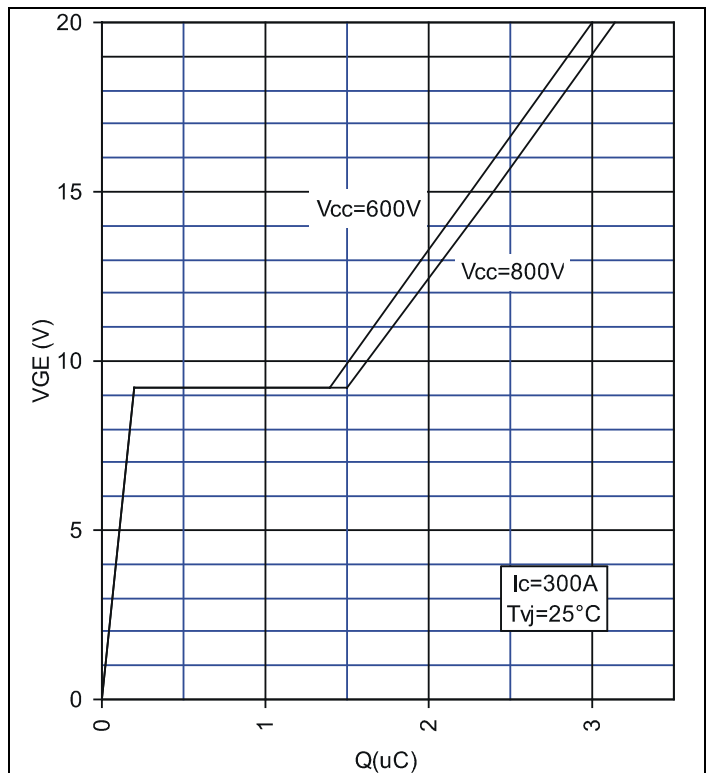
**Fig. 1** Typ. Output Characteristics at  $T_j=25^\circ\text{C}$



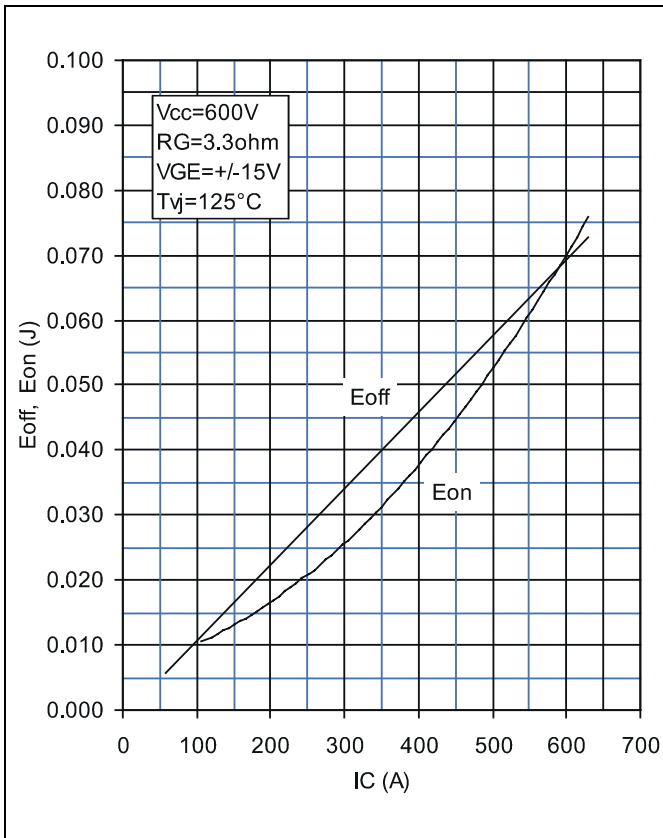
**Fig. 2** Typ. Output Characteristics at  $T_j=125^\circ\text{C}$



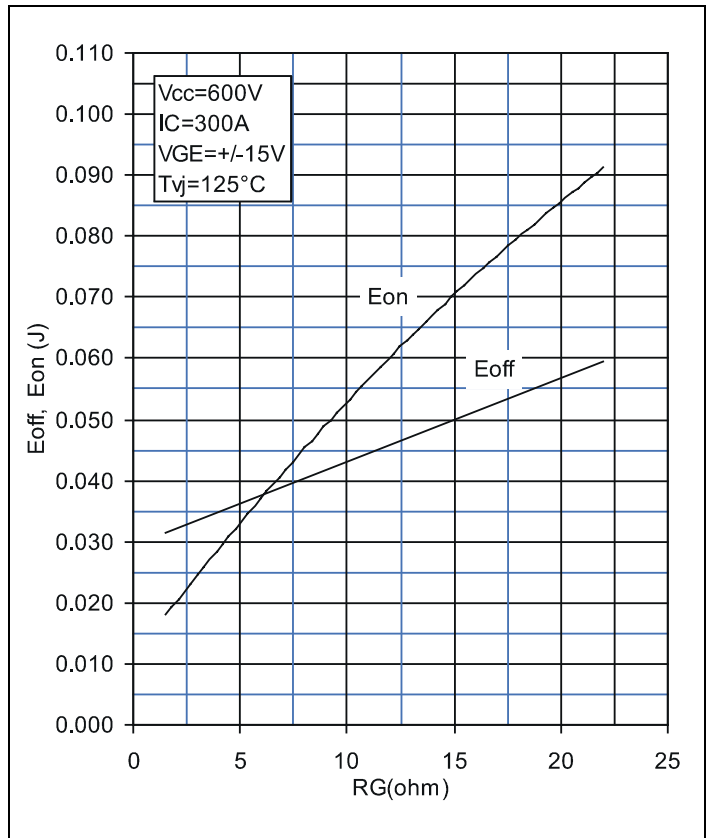
**Fig. 3** Typ. Transfer Characteristics



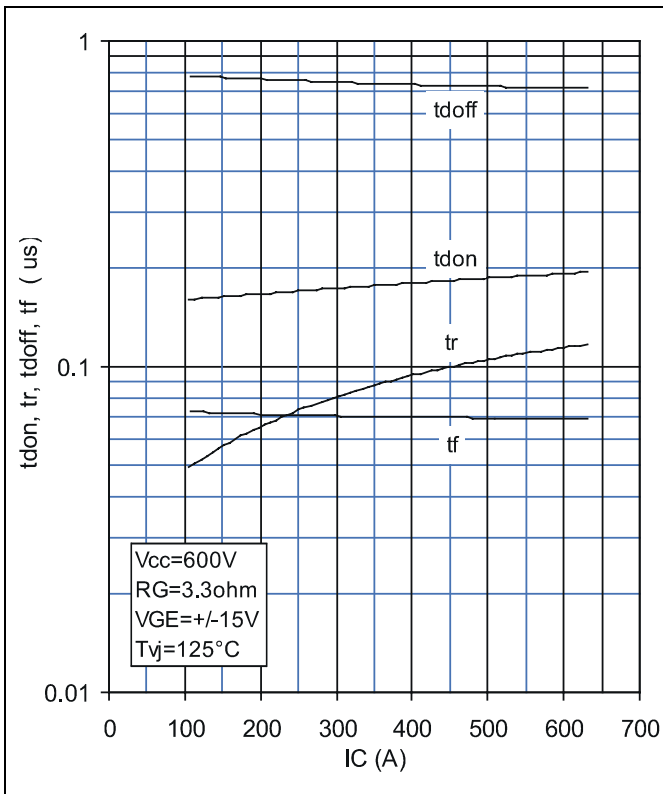
**Fig. 4** Typ. Gate charge Characteristics



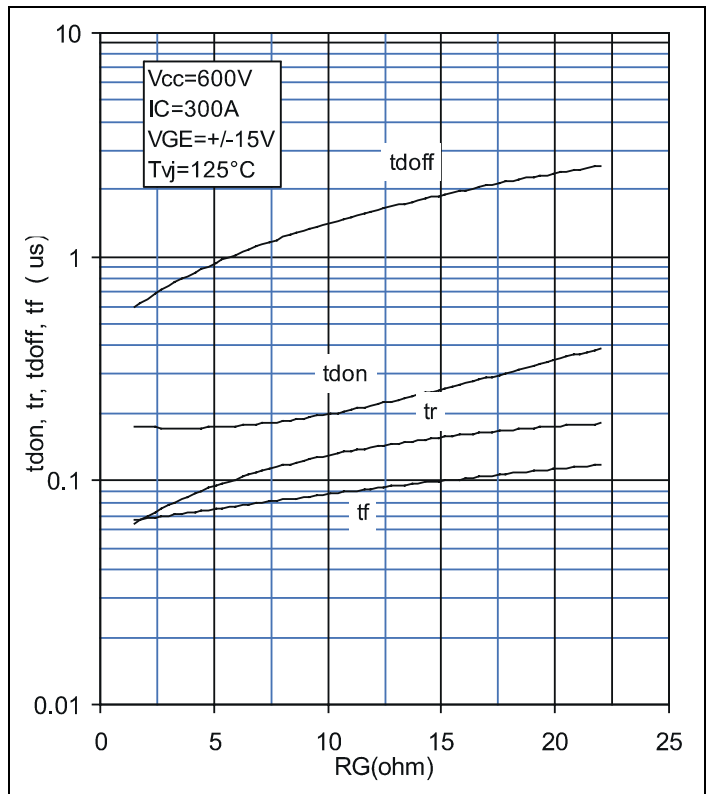
**Fig. 5** Typ. Switching Energies per pulse vs on-state current



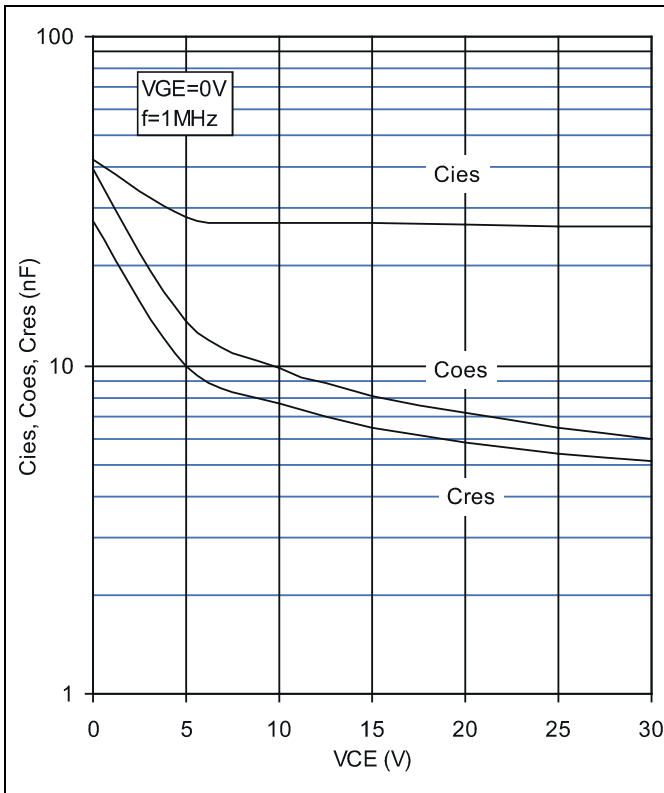
**Fig. 6** Typ. Switching Energies per pulse vs gate resistor



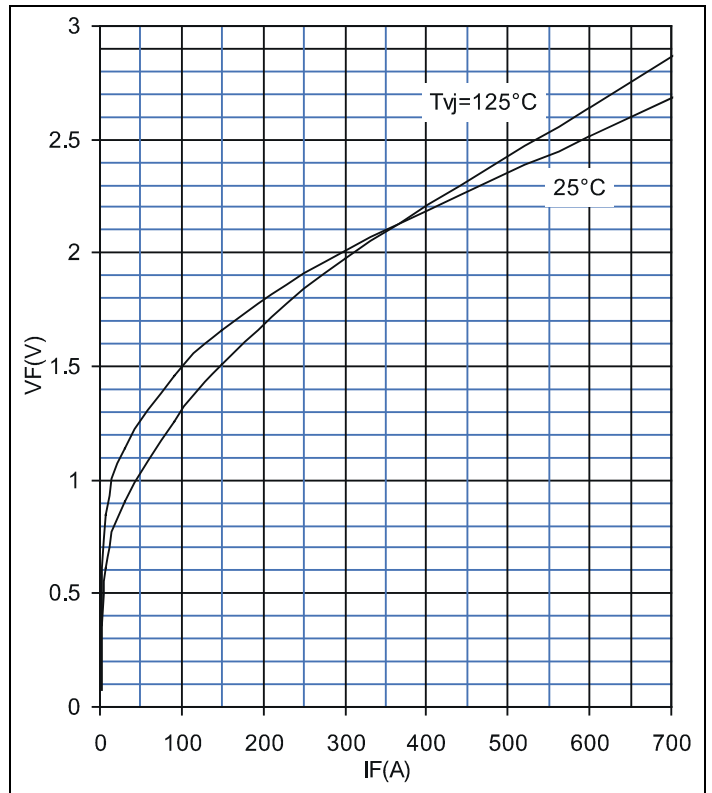
**Fig. 7** Typ. Switching times vs on-state current



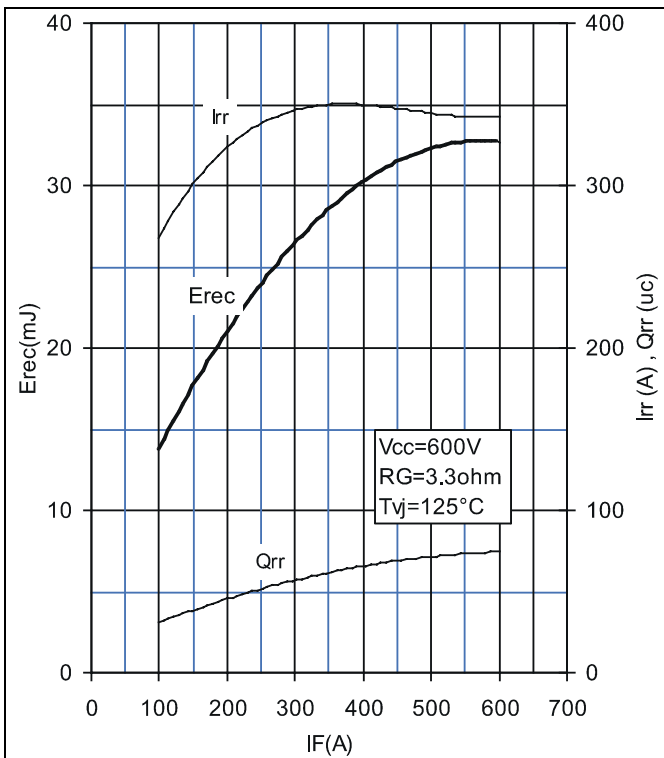
**Fig. 8** Typ. Switching times vs gate resistor



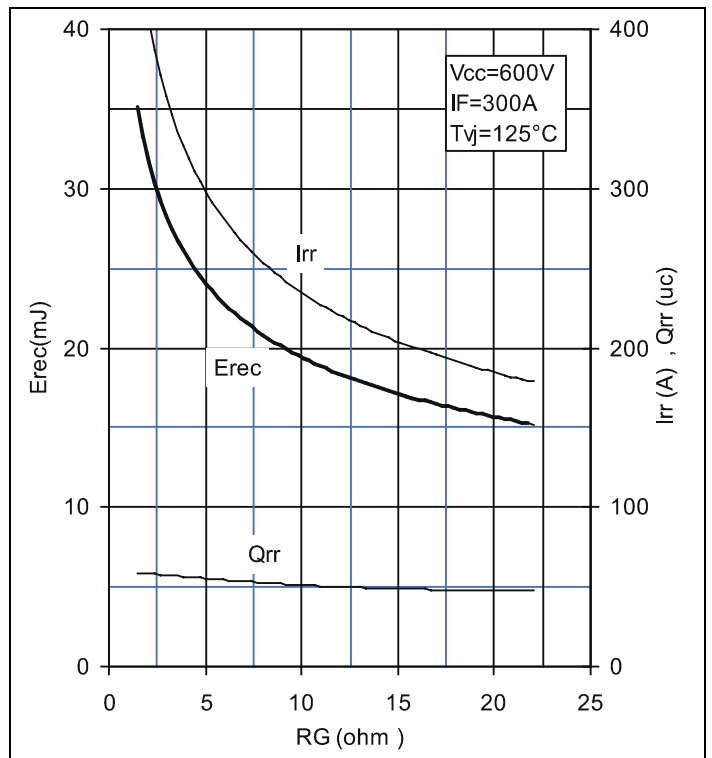
**Fig. 9** Typ. Capacitances vs collector-emitter Voltage



**Fig. 10** Typ. Diode forward Characteristics



**Fig. 11** Typ. Reverse Recovery Characteristics vs forward current



**Fig. 12** Typ. Reverse Recovery Characteristics vs gate resistor

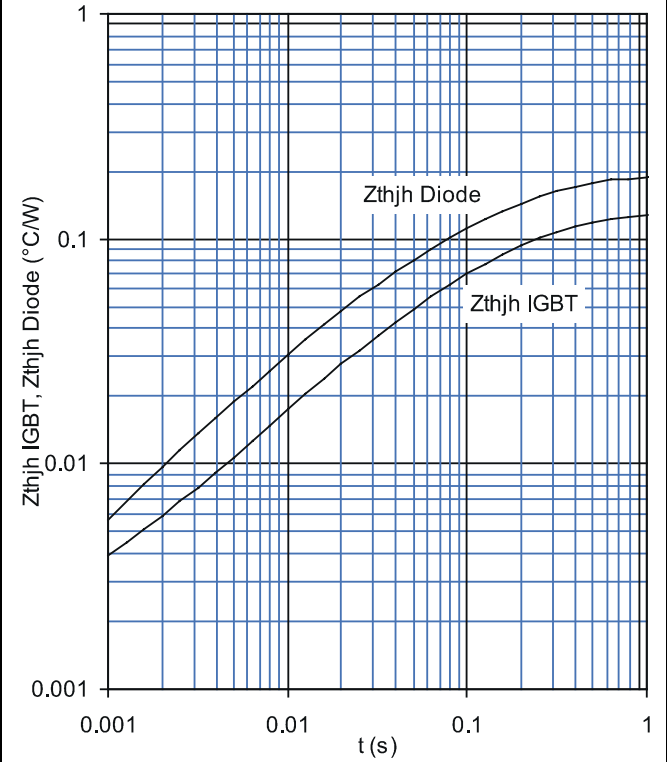


Fig. 13 Typ. Thermal impedance vs time

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